

525 305
23 FEB 2005

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
11 March 2004 (11.03.2004)

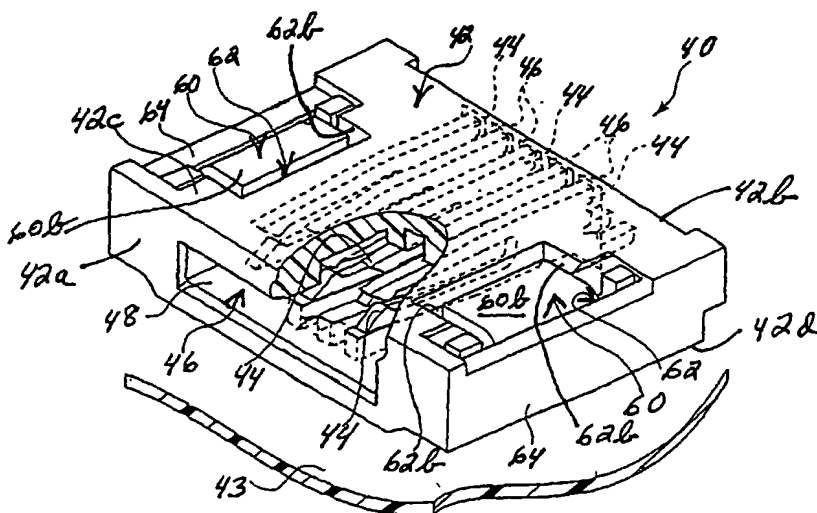
PCT

(10) International Publication Number
WO 2004/021518 A1

- (51) International Patent Classification⁷: **H01R 12/20** 1-24-11-201 Sonan, Sagamihara-shi, Kanagawa 228-0012 (JP).
- (21) International Application Number: PCT/US2003/026529 (74) Agent: WEISS, Stephen Z.; Molex Incorporated, 2222 Wellington Court, Lisle, IL 60532 (US).
- (22) International Filing Date: 25 August 2003 (25.08.2003) (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZM, ZW.
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
2002-255357 30 August 2002 (30.08.2002) JP
2003-133491 12 May 2003 (12.05.2003) JP
- (71) Applicant (for all designated States except US): **MOLEX INCORPORATED** [US/US]; 2222 Wellington Court, Lisle, IL 60532 (US).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **KUNISHI, Shin-suke** [JP/JP]; 12-1 Minami-yama, 5-Chome, Hadano-shi, Kanagawa 257-0003 (JP). **HAYASHI, Masanori** [JP/JP];
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- Published:
— with international search report

[Continued on next page]

(54) Title: ELECTRICAL CONNECTOR FOR INTERCONNECTING FLAT CIRCUITS



(57) Abstract: An electrical connector is provided for interconnecting a pair of flat circuits and includes a housing for mounting on a first flat circuit. The housing has a cavity for receiving a second flat circuit. A through passage extends from a top face of the housing through a bottom face thereof. A plurality of terminals are mounted in the housing along the cavity. The terminals have contact portions for engaging appropriate circuit contacts on the second flat circuit when the second flat circuit is inserted into the cavity. A reinforcing member includes a body portion inserted into the through passage in the housing from the top face thereof and includes a foot portion exposed at the bottom face of the housing for securing to the first flat circuit. The reinforcing member has a locking portion extending obliquely to the body portion and engageable with a locking surface on the housing.

WO 2004/021518 A1



— *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.